

# All SiC Module for Traction Inverters with 1<sup>st</sup> Generation Trench Gate SiC MOSFETs

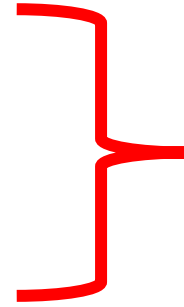
Chen Song, Fuji electric China Co., Ltd

1. *Introduction*
2. *Future of Package for Traction*
3. *3.3kV 1<sup>st</sup> gen trench gate MOSFETs technologies*
4. *Electrical performance for SiC MOSFETs*
  - 4-1 Static characteristics
  - 4-2 Improvement of leakage current
  - 4-3 Dynamic characteristics
  - 4-4 PWM simulation results
  - 4-5 Calculation results for relationship between  $T_{vj}$  and  $I_o$
5. *Line up plan*
6. *Conclusion: Down sizing*

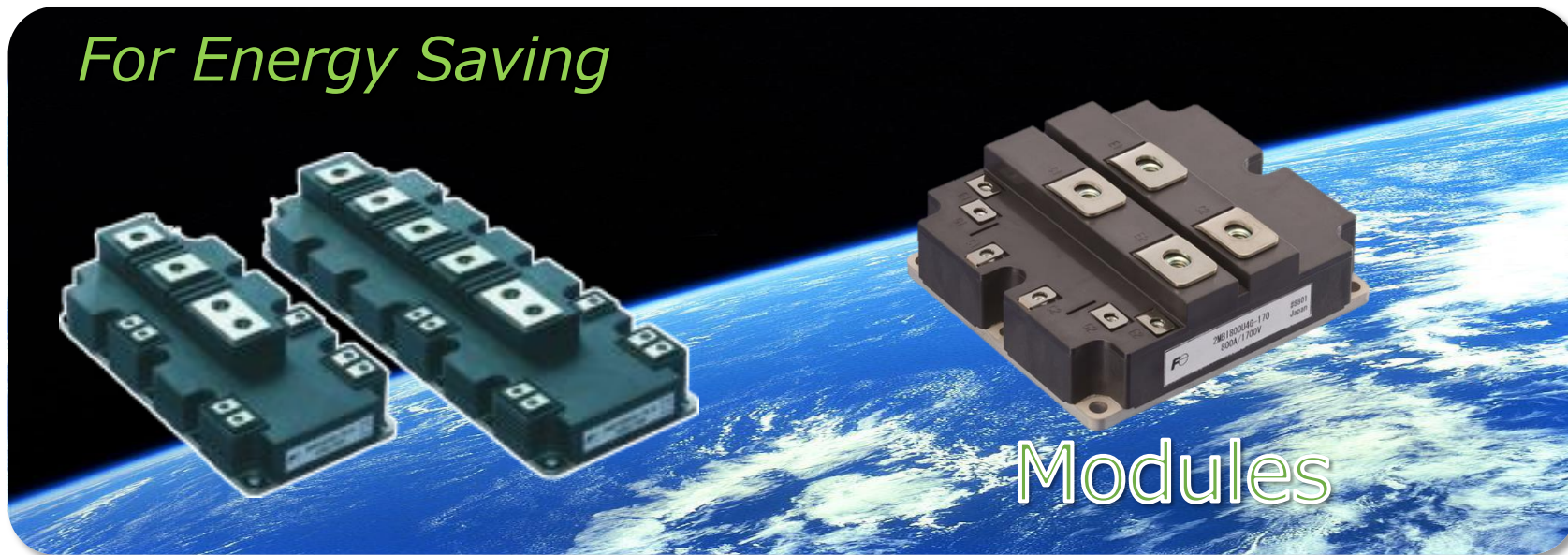
# 1. Introduction

## Power semiconductor Module's Market Demands

- ✓ High current density
- ✓ Low dissipation

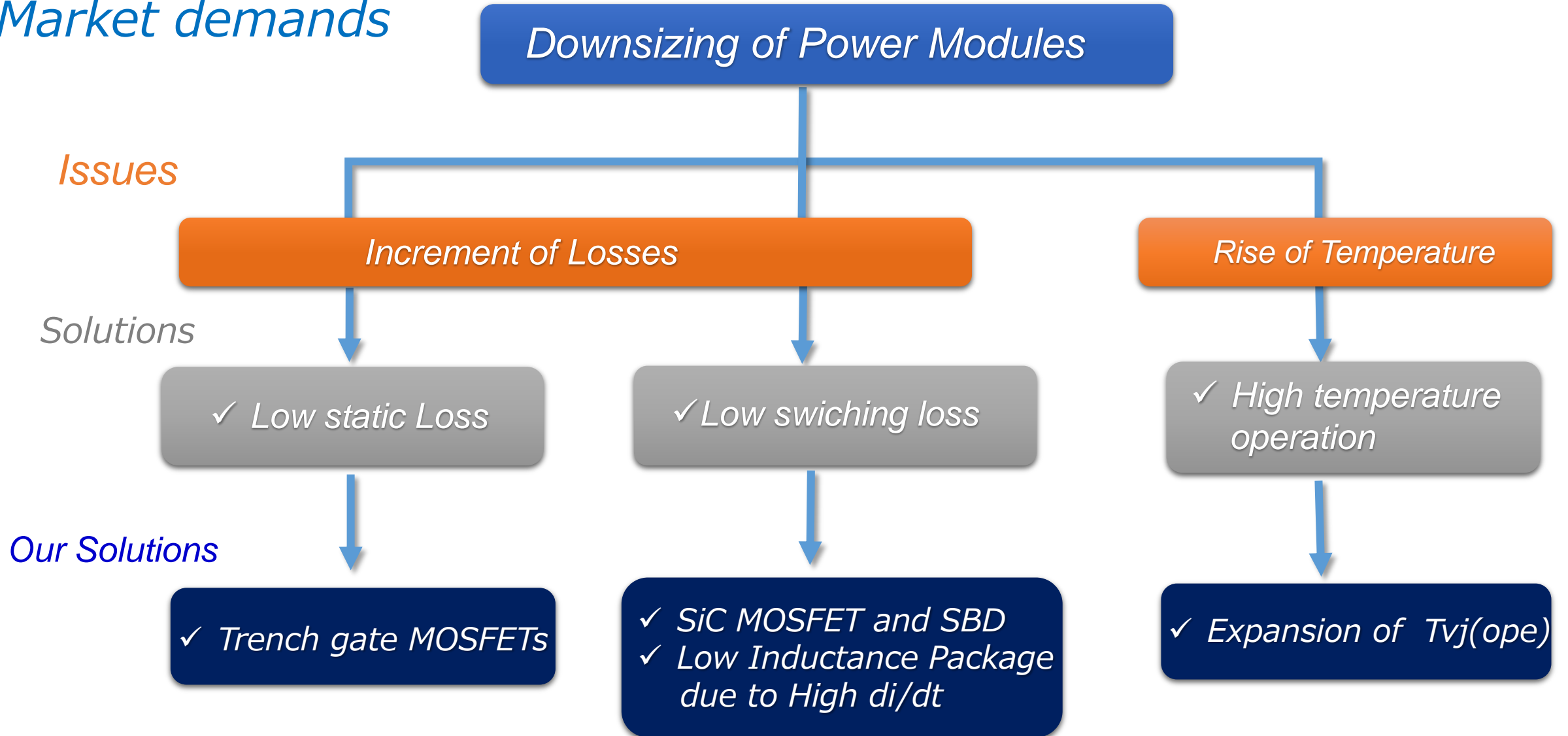


*Down sizing*



# 1. Introduction

## Market demands

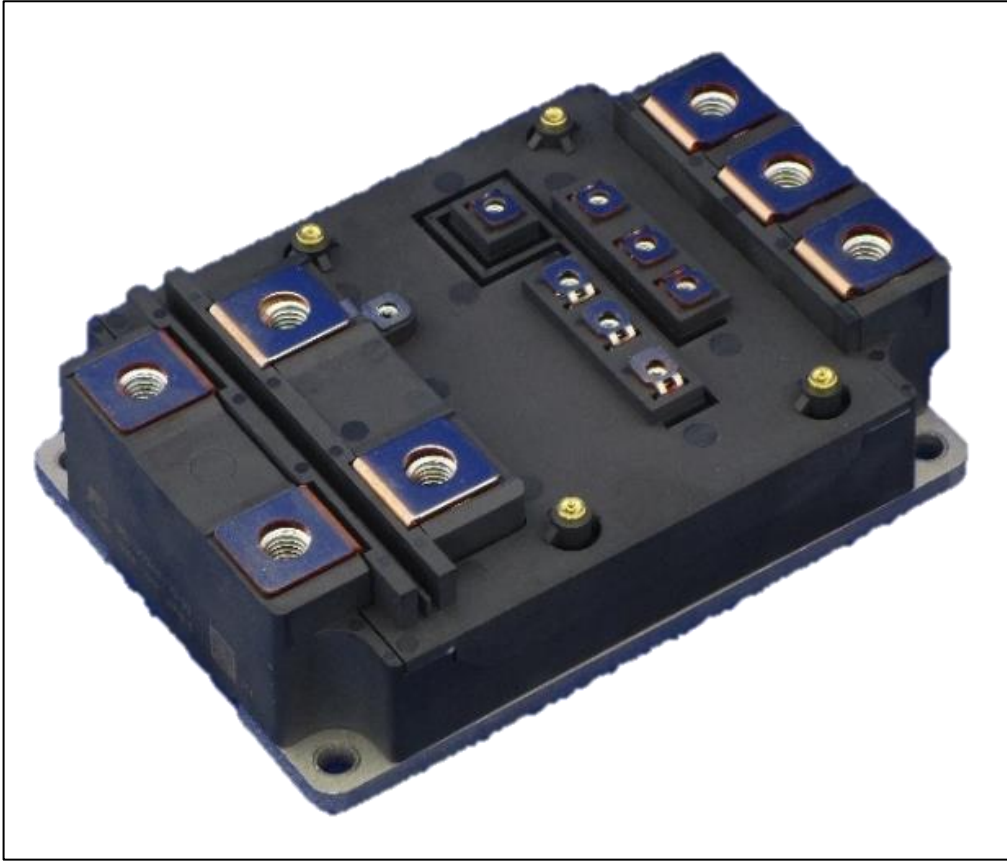


\* $T_{vj(ope)}$ : Continuous Operating Junction Temperature

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## 2. Feature of Package for Traction

*HPnC : New PKG*



*HPM : Conventional PKG*



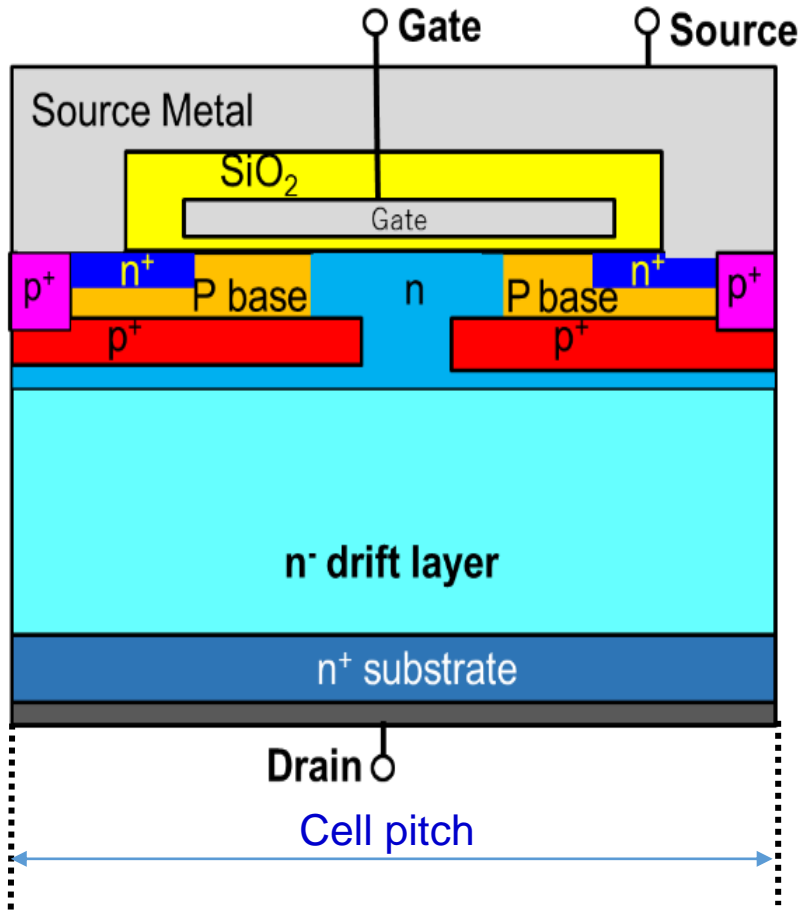
- *Low inductance: 10nH*
- *High isolation capability up to 3.3kV*
- *Traction standard: EN50124-1 and EN45545-2*

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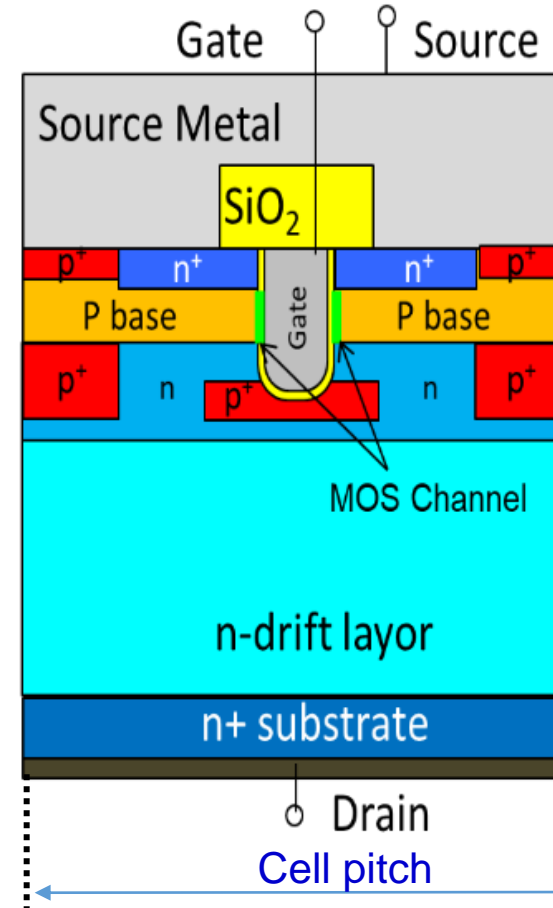
### 3. 3.3kV 1<sup>st</sup> gen trench gate MOSFETs technologies

#### Cross-Section Structure of MOSFETs

#### Planar SiC MOSFET



#### 1G Trench SiC MOSFET



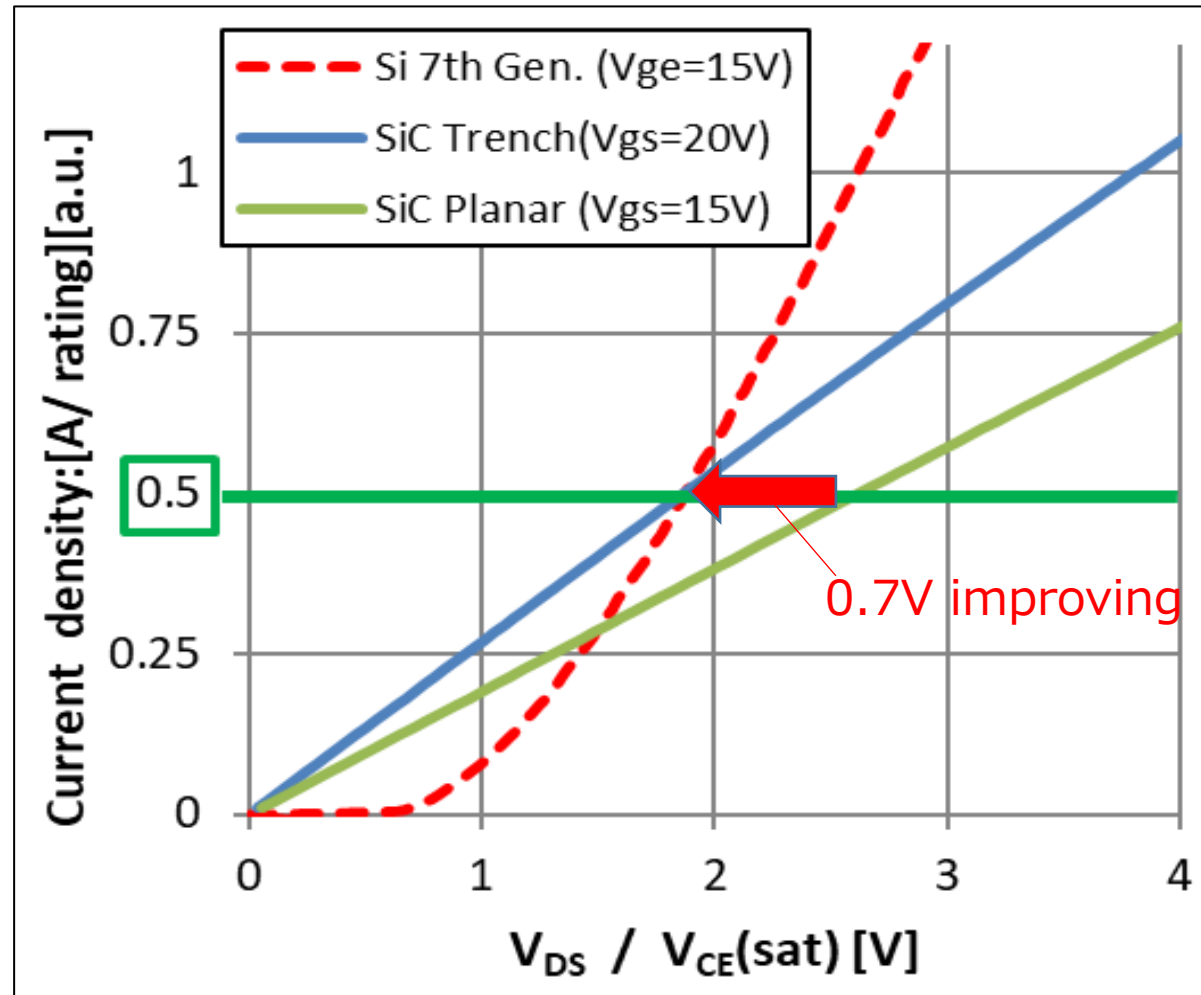
Cell pitch of 1G trench Gate was reduced by 50% and RonA could be reduced 40% compared to planar Gate.



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## 4-1. Static characteristics

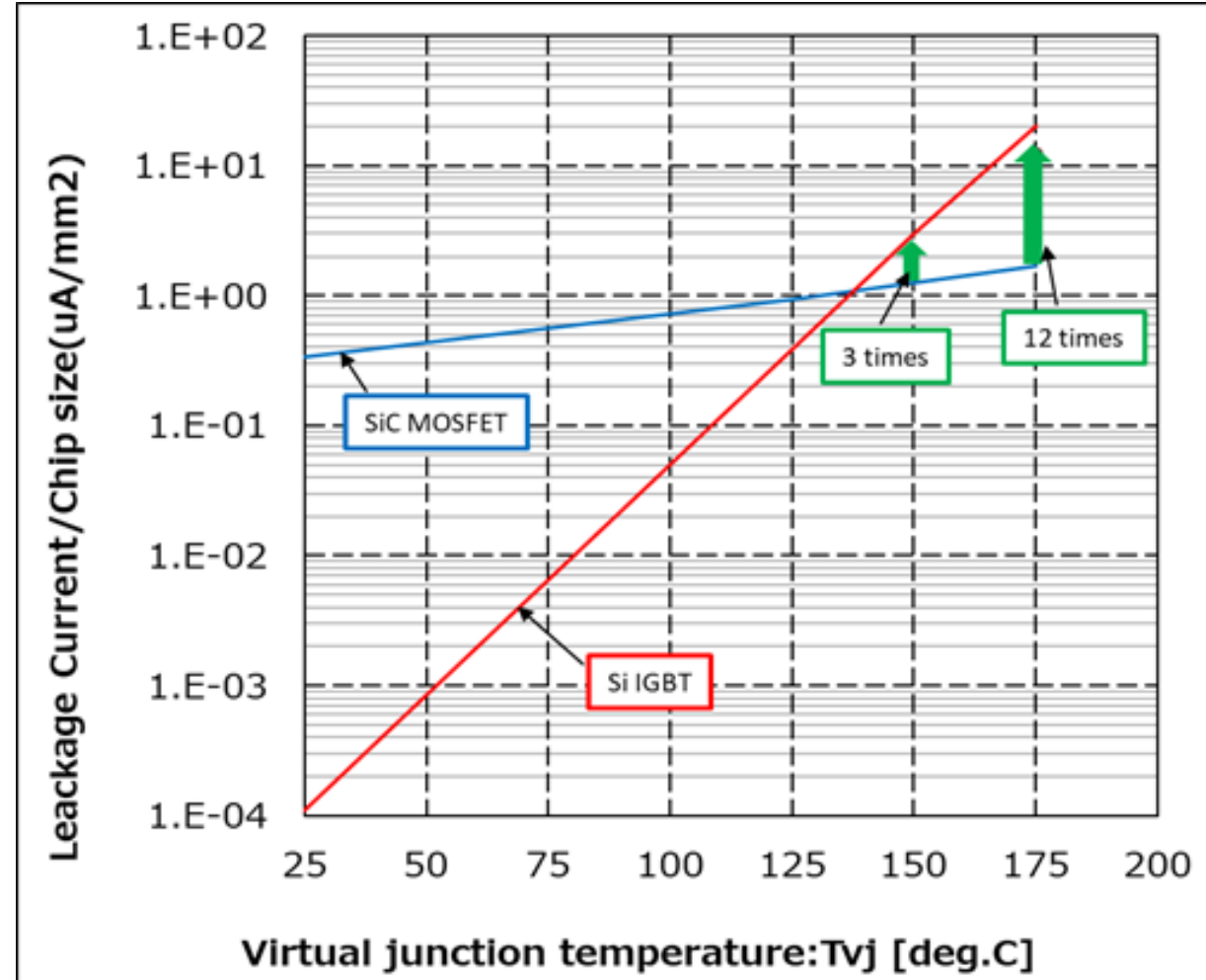
Si 7th Gen./SiC1<sup>st</sup> Gen. trench/SiC planar. output characteristic at 150deg.C



All-SiC trench 1G gate is lower than  $V_{ce}$  of Si 7<sup>th</sup> Gen under half rated current, which is used under general continuous operation condition

## 4-2. Improvement of leakage current

Si 7<sup>th</sup> gen IGBT and SiC MOSFET leakage current

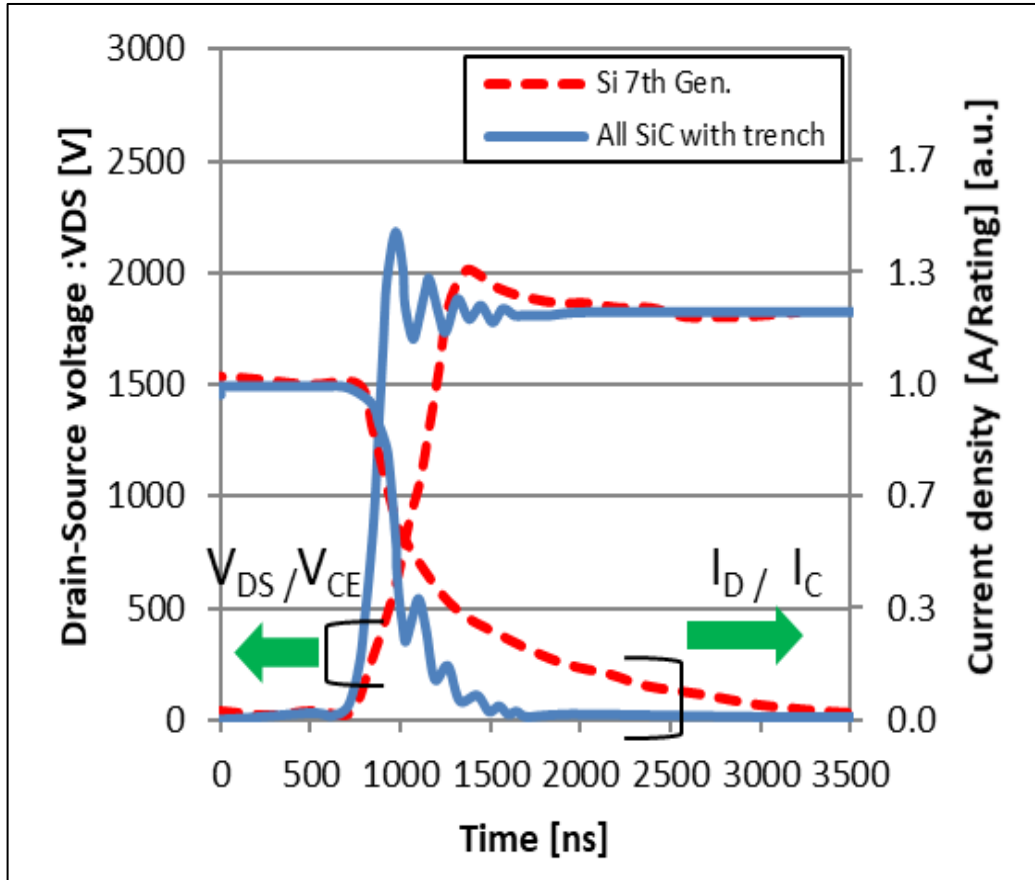


- Leakage current of Si IGBT was approximately 3 times larger than SiC MOSFET at 150deg.C and enable to operate under higher temperature.
- Maximum temperature is limited to 175deg.C due to Package reliability.

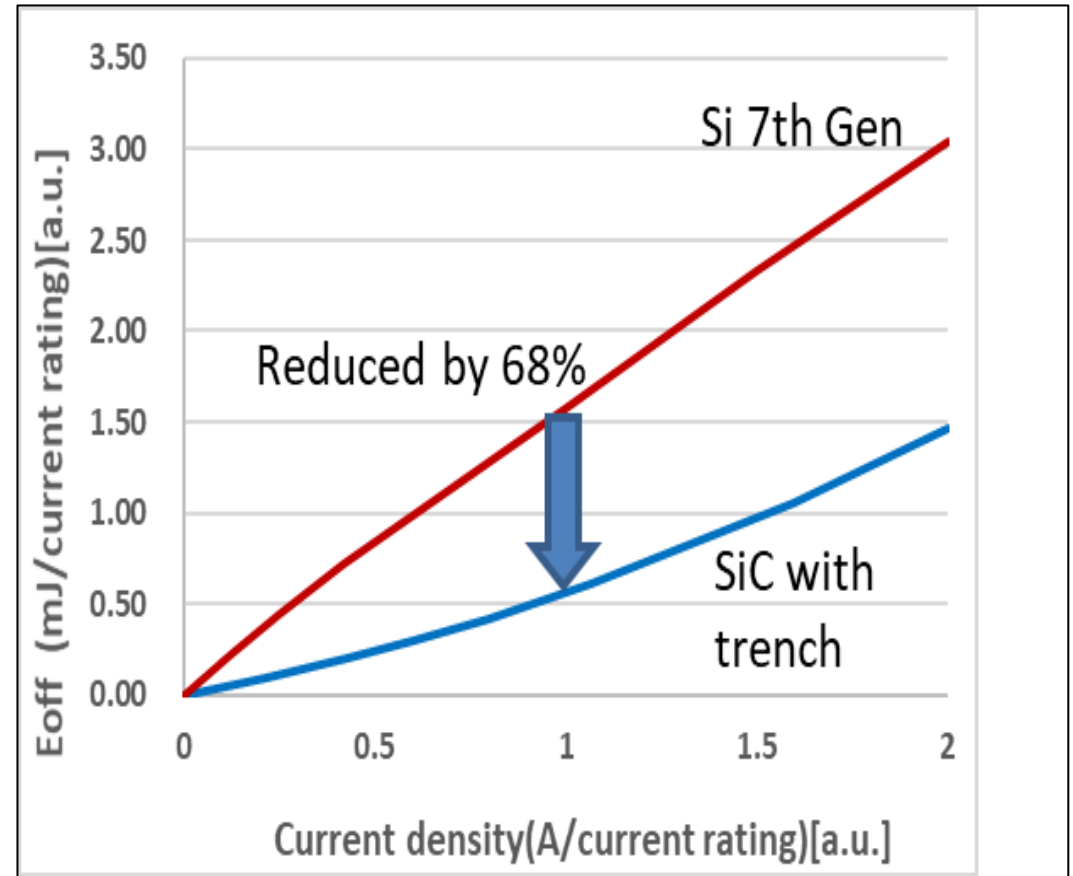
## 4-3. Dynamic characteristics Turn off

$V_{cc}=1800V$ ,  $I_D$ =same current density(A/rating),  $R_g=2.7\Omega$  for Si 7<sup>th</sup> Gen ,  $R_g=10\Omega$  for All SiC

Switching waveforms



E<sub>off</sub>



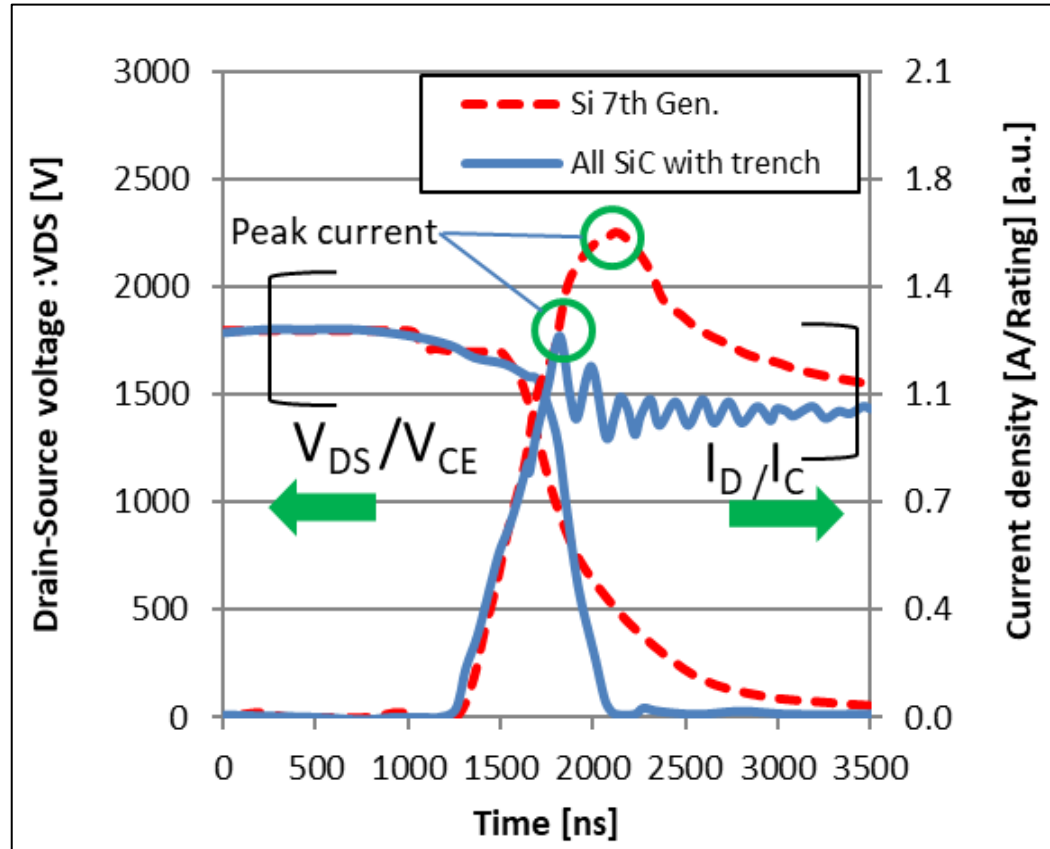
The tail current of all-SiC module was reduced and the rising voltage was also faster compared to 7th generation Si IGBT module.

→ E<sub>off</sub> was reduced by 68%.

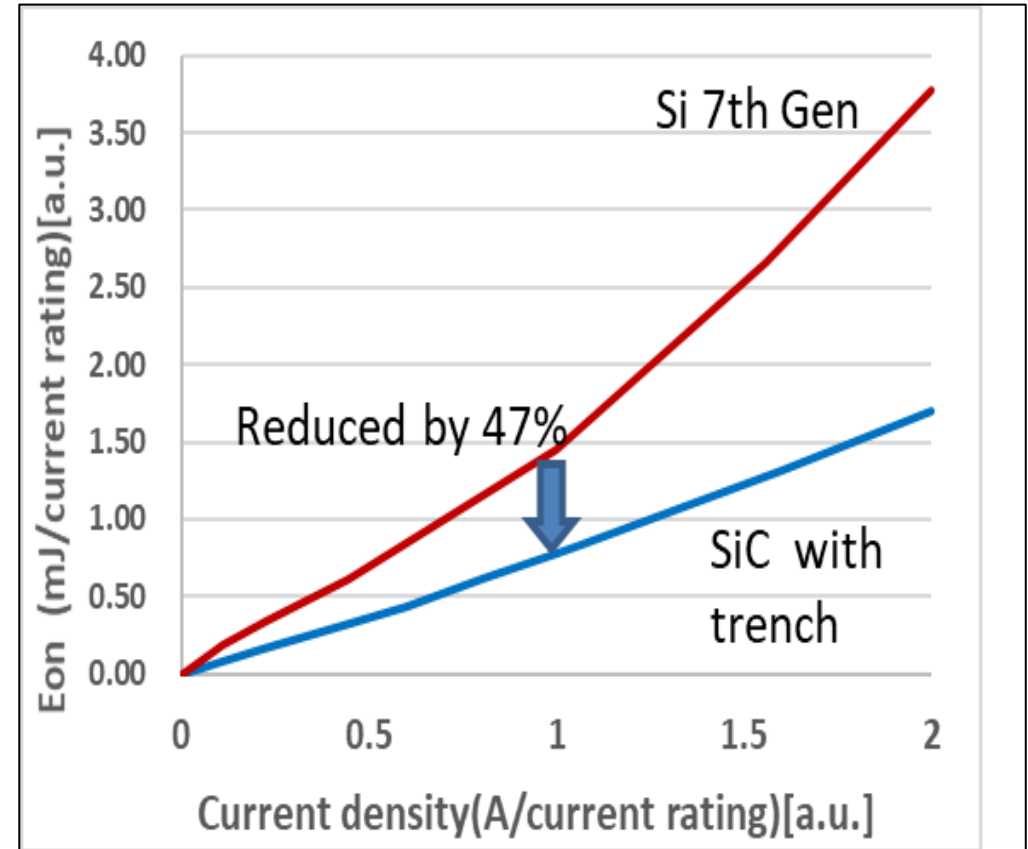
## 4-3. Dynamic characteristics Turn on

$V_{cc}=1800V$ ,  $I_D$ =same current density(A/rating),  $R_g=0.68\Omega$  for Si 7<sup>th</sup> Gen ,  $R_g=20\Omega$  for All SiC

### Switching waveforms



### Eon



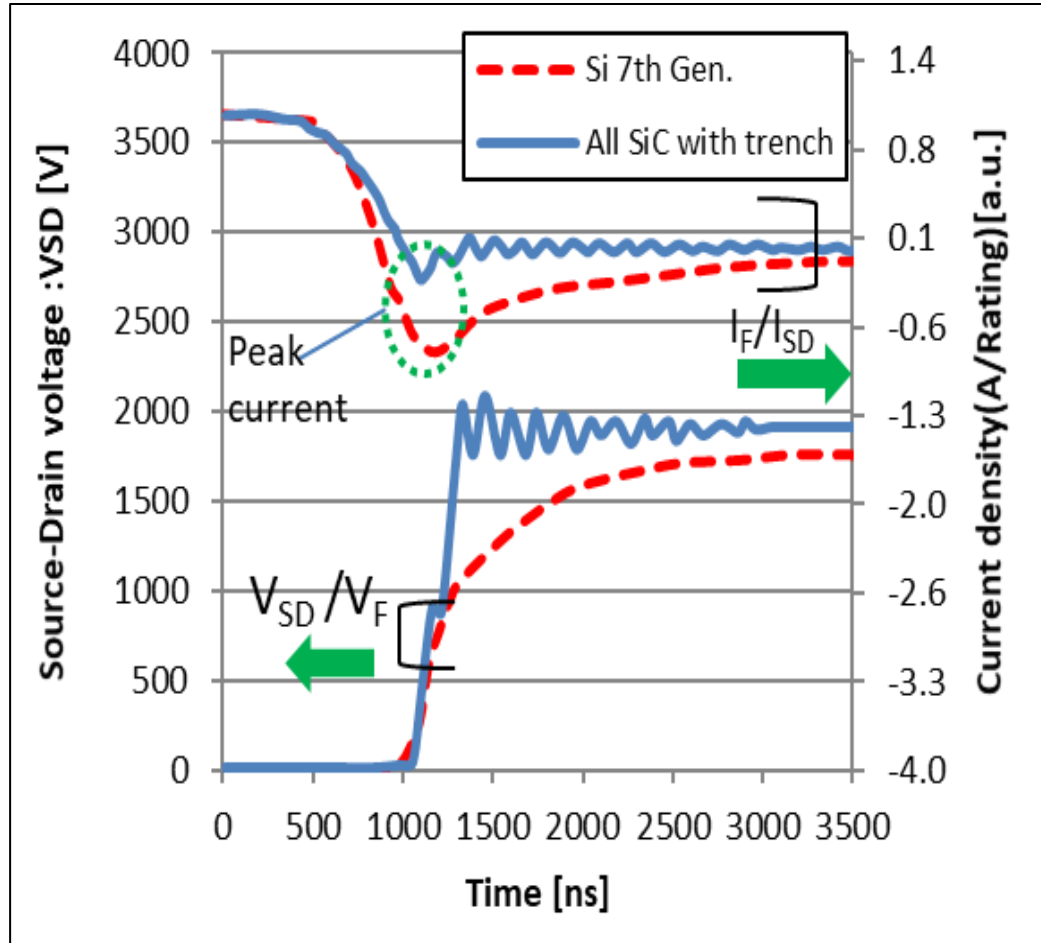
The peak current at turn on of the all-SiC module was reduced compared to 7th generation Si IGBT module.

→ Eon was reduced by 47%.

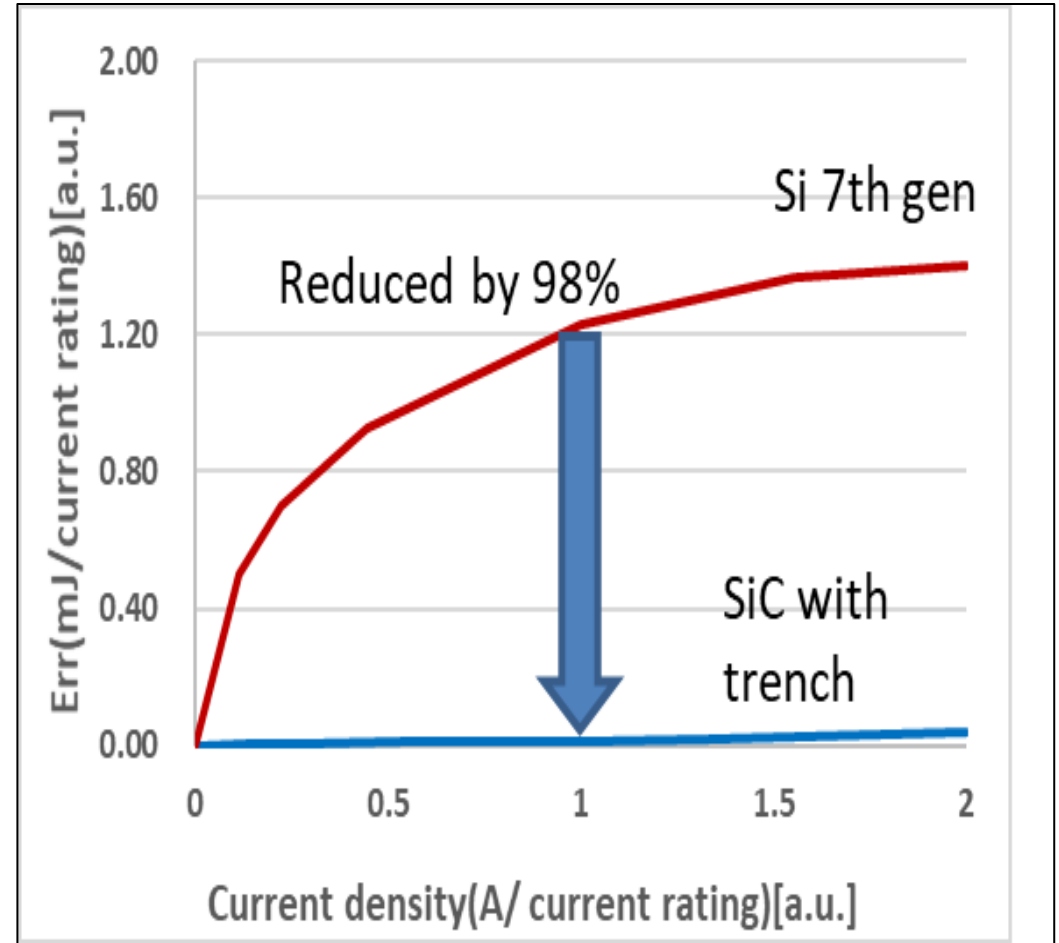
# 4-3. Dynamic characteristics Recovery

$V_{cc}=1800V$ ,  $I_D$ =same current density(A/rating),  $R_g=0.68\Omega$  for Si 7<sup>th</sup> Gen ,  $R_g=20\Omega$  for All SiC

Switching waveforms



Err

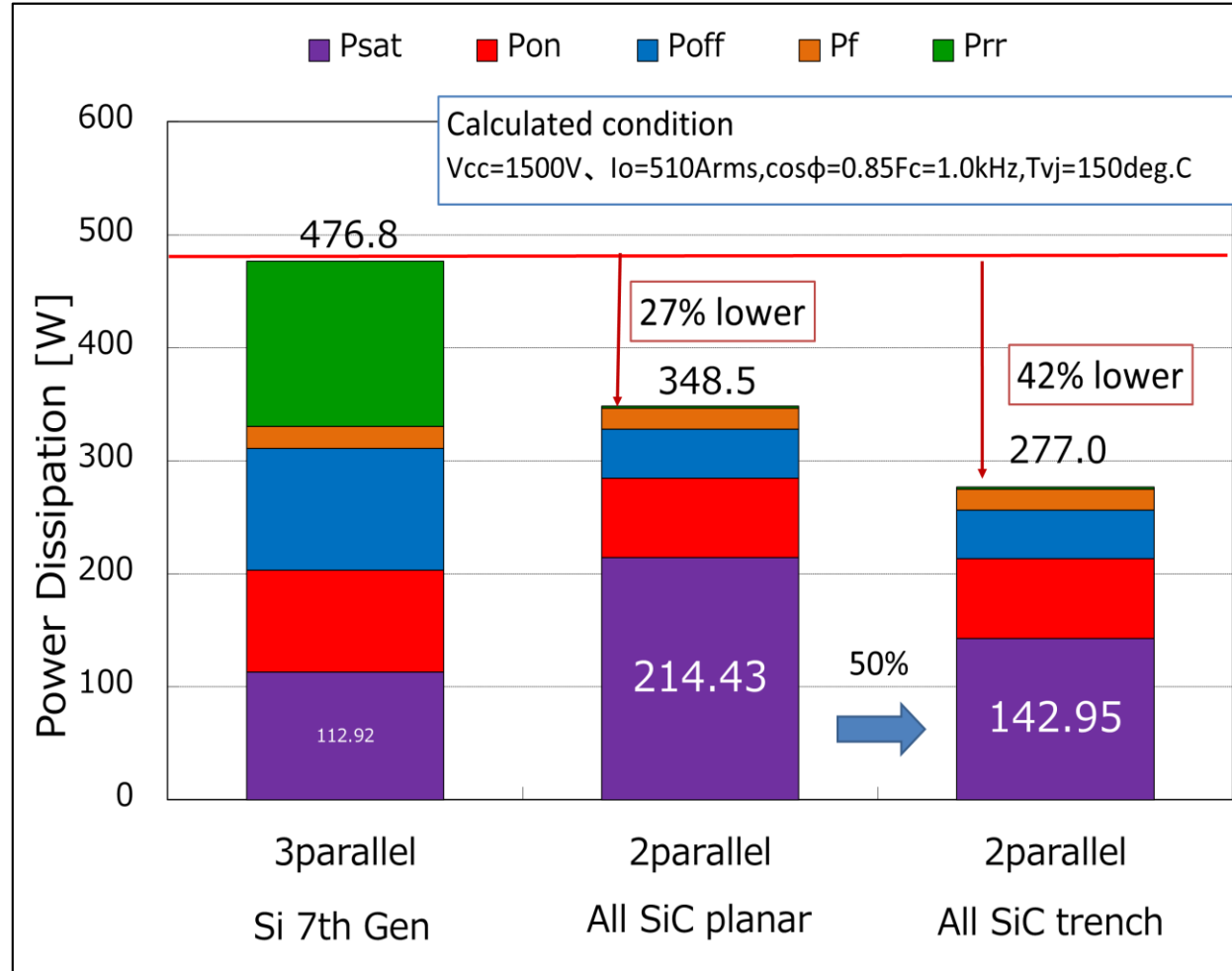


The tail current of all-SiC module was reduced compared to the 7<sup>th</sup> generation Si IGBT module.  
→ Err was reduced by 98%.

# 4-4. PWM simulation results

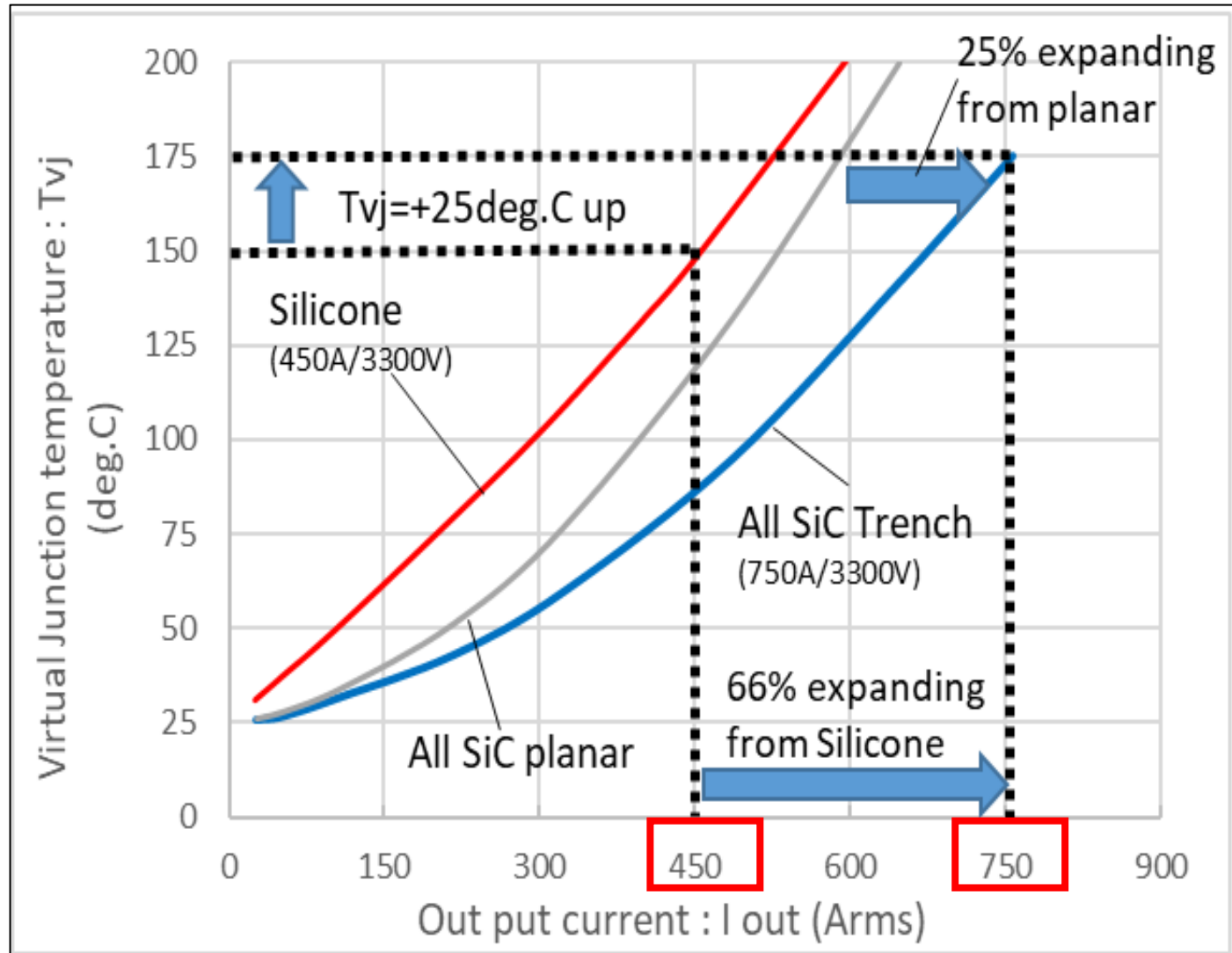
Calculate condition :  $V_{cc}=1500V, I_o=510A_{rms}, \cos\phi=0.85, \lambda=1.0, F_c=1kHz, T_{vj}=150deg.C$

Sample rating : 450A/3300V(Si maximum rating) , 750A/3300V(All SiC maximum rating)



The overall power dissipation for All SiC module with trench gate with 2 modules in parallel was reduced 42% compared to Si 7<sup>th</sup> Gen.

# 4-5. Calculation results for relationship between $T_{vj}$ and $I_o$







All SiC module with Trench gate MOSFET realized to expand the  $I_o$  by 66% with same package size during overload operation.



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# 5. Lineup plan

Package		Small 1B	Small 2B	62mm STD	HPnC		
size(WxDxH)		62.8x33.8x12 mm	56.7x62.8x12 mm	62x108x30.5 mm	100x140x38 mm		
Rated voltage	Equivalent Circuit						
		1200V	2in1 ~100A	6in1 ~50A	~100A	~200A	~600A
1700V	2in1	-	-	~400A	TBD		
3300V	2in1	-	-	-	750A		
<b>Application</b>		<b>Auxially equipment</b>			<b>Propulsion</b>		

The product lineup will realize coverage of the wide range for traction.

## 6. Conclusion: **Down sizing**

- *High temperature operation*  
 → Operation temperature was expanded to 175deg.C
- *Low Power Dissipation*  
 → Dissipation loss was decreased by 42%



- *Down sizing (High Power Density)*  
 → Output current was expanded by 66%  
 → Maximum rated current was expanded from 450A to 750A

*All-SiC module will contribute greatly to the high efficiency and miniaturize power conversion systems in the future.*

*Thank you very much!!*

**FE** Fuji Electric  
*Innovating Energy Technology*